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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application No.	10/821,465
Filing Date	April 9, 2004
First Named Inventor	William A. Burkland
Art Unit	not yet known
Examiner Name	not yet known
Attorney Docket Number	21901-07984

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document No. Number - Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
<i>W</i>		FELDMAN, A., "A 13-Bit, 1.4-MS/s Sigma-Delta Modulator for RF Baseband Channel Applications", IEEE Journal of Solid State Circuits, October 1998, Vol. 33, No. 10, pp. 1462-1469	
<i>h</i>		HOFFMAN, A., ET AL., "Mathematical Time Approach of a First Order Sigma-Delta Modulator", 02-09-1998, pp. 1-4	
<i>l</i>		JARMAN, D., "A Brief Introduction to Sigma Delta Conversion", Intersil Corporation, May 1995, pp. 1-7	
<i>a</i>		"Digital-analog converters are a "bit" analog", Maxim, May 2003, pp. 40, 42.	
<i>v</i>		"About oversampling, noise shaping, digital filtering, and decimation", Maxim, May 2003, pp. 38-39	

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Examiner Signature	<i>William A. Burkland</i>	Date Considered	12/20/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809.

Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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